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JULPatent
Attorney's Docket No. 032481-021

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Prakash Patel

Application No.: 09/687,777

Filed: October 13, 2000

For: JAVA HARDWARE ACCELERATOR
USING MICROCODE ENGINE

Group Art Unit: unknown

Examiner: unknown

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SEP 14 2001**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
TRANSMITTAL LETTER**Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

No additional fee for submission of an IDS is required.

The fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.

A certification under 37 C.F.R. § 1.97(e) is also enclosed.

A certification under 37 C.F.R. § 1.97(e), and the fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) are also enclosed.

Charge \$_____ to Deposit Account No. 02-4800 for the fee due.

A check in the amount of \$_____ is enclosed for the fee due.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By:


Joseph P. O'Malley
Registration No. 36,226
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Date: September 7, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Mukesh Patel

Application No.: 09/687,777

Filed: October 13, 2000

For: JAVA HARDWARE ACCELERATOR
USING MICROCODE ENGINE

) Group Art Unit: unknown

) Examiner: unknown

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, hereby Applicant submits the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

Dickol, et al., USPN 5,875,336, describes a method and system for translating a non-native bytecode to a set of codes native to a processor.

Yellin, et al., USPN 5,999,731, utilizes a program interpreter for computer programs written in a bytecode language, which uses a restricted set of data type specific bytecodes.

Koppala, USPN 6,131,144, describes a stack management unit including a stack cache to accelerate data retrieval and data storage.

Koppala 6,167,488, describes a stack management unit including a stack cache to accelerate data retrieval and data storage.

Lueh et al. 6,158,048, describes a method for eliminating common subexpressions from Java bytecodes.

"SGI WebForce 02 is a one-stop Web authoring platform," InfoWorld, January 20, 1997, describes a Java-based web server system.

Krall, et al., "CACAO - A 64-bit Java VM just-in-time compiler," Concurrency: Practice and Experience, Vol. 9 (11), pp. 1017-1030, November 1997, describes software interpretation techniques.

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The documents are being submitted within 3 months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later, therefore no fee or certification is required under 37 C.F.R. § 1.97(b).

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By: _____
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